Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L4	2	(("6664801") or ("6441633")).PN.	USPAT	OR	OFF	2005/02/17 18:25
L5	2	("5731700" | "6118293").PN. OR ("6664801").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/02/17 18:26
L6	2	("5332973" | "6118293").PN. OR ("6441633").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/02/17 18:42
L9	7	manhaeve.in.	US-PGPUB; USPAT	OR	OFF	2005/02/17 19:11
L10	4	("6087843" | "6118293" | "6239604").PN. OR ("6496028").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/02/17 19:15
L11	11	("5332973" | "5731700" | "5808476" | "6144214" | "6169408" | "6195772" | "6301168" | "6342790" | "6414511" | "6496028" | "6518782").PN. OR ("6833724").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/02/17 19:17
S1	3	"5818268"	US-PGPUB; USPAT	OR	ON	2004/12/08 18:17
S2	4	(("5818268") or ("6414511") or ("5914615") or ("5483170")).PN.	US-PGPUB; USPAT	OR	OFF	2004/12/08 18:23
S3	164	324/763,765.ccls. and (iddq quiescent)	US-PGPUB; USPAT	OR	OFF	2004/12/08 18:38
S4	16	mosfet and S3	US-PGPUB; USPAT	OR	OFF	2004/12/08 18:25
S5	10	257/48.ccls. and (iddq quiescent)	US-PGPUB; USPAT	OR	OFF	2005/02/14 16:34
S6	1071	mosfet and (iddq quiescent)	US-PGPUB; USPAT	OR	OFF	2004/12/08 18:41
S7	46	mosfet and (iddq quiescent) and "324"/$.ccls.	US-PGPUB; USPAT	OR	OFF	2004/12/08 18:50
S8	25	mosfet and (iddq quiescent) and "438"/$.ccls.	US-PGPUB; USPAT	OR	OFF	2004/12/08 18:51
S9	2	("5731700" | "6118293").PN. OR ("6664801").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2004/12/08 18:54
S10	418	"714"/$.ccls. and (iddq quiescent)	US-PGPUB; USPAT	OR	OFF	2004/12/08 19:03
S11	10	"714"/$.ccls. and (iddq quiescent) and mosfet	US-PGPUB; USPAT	OR	OFF	2004/12/08 19:15
S12	11	((inverter follwer) and (iddq quiescent) and mosfet).clm.	US-PGPUB; USPAT	OR	OFF	2005/02/17 19:11
S13	1	(charge adj transfer adj effects and mosfet)	US-PGPUB; USPAT	OR	OFF	2004/12/09 12:33

S14	1	(minimis$4 with charge adj transfer adj effects and mosfet)	US-PGPUB; USPAT	OR	OFF	2004/12/09 12:33
S15	93	(charge adj transfer with mosfet)	US-PGPUB; USPAT	OR	OFF	2004/12/09 12:34
S16	32	charge adj transfer with mosfet same capacitor	US-PGPUB; USPAT	OR	OFF	2004/12/09 12:37
S17	0	minimis$4 with charge adj transfer with mosfet same capacitor	US-PGPUB; USPAT	OR	OFF	2004/12/09 12:38
S18	1	minimis$4 with charge adj transfer with mosfet	US-PGPUB; USPAT	OR	OFF	2004/12/09 12:38
S19	0	minimis$4 with charge adj transfer with capacitor same (inverter follower)	US-PGPUB; USPAT	OR	OFF	2004/12/09 12:38
S20	0	minimis$4 with charge adj transfer with capacitor same (inverter follower)	US-PGPUB; USPAT	OR	ON	2004/12/09 12:38
S21	0	minimis$4 with charge adj transfer with capacitor	US-PGPUB; USPAT	OR	ON	2004/12/09 12:38
S22	6	minimis$4 with charge adj transfer	US-PGPUB; USPAT	OR	ON	2004/12/09 12:49
S23	0	minimis$4 with charge adj transfer	USOCR	OR	ON	2004/12/09 12:49
S24	17	minimis$4 with charge adj transfer	EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/09 12:50
S25	18	(IDDq with (measur$4 determin$4)).clm.	USPAT	OR	OFF	2005/02/14 15:55
S26	2518	324/763,765,769.ccls.	USPAT	OR	OFF	2005/02/14 15:55
S27	90	S26 and IDDQ	USPAT	OR	OFF	2005/02/14 15:55
S28	175	324/763,765,769.ccls. and (iddq quiescent)	US-PGPUB; USPAT	OR	OFF	2005/02/14 16:37
S29	61	324/763,765,769.ccls. and (iddq quiescent) and (inverter follwer)	US-PGPUB; USPAT	OR	OFF	2005/02/14 16:46
S30	46	324/763,765,769.ccls. and (current with bypass)	US-PGPUB; USPAT	OR	OFF	2005/02/14 16:47